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Nicole Barrese 6/15/05
(Signature & date)

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:
Jed H. Rankin *et al.*

Examiner: Not yet assigned

Application No.: Not yet assigned

Group Art Unit: Not yet assigned

Filed: Herewith

For: INTEGRATED ANTIFUSE
STRUCTURE FOR FINFET AND
CMOS DEVICES

Date: June 15, 2005

This application is the U.S. national stage
under 35 U.S.C. § 371 of
International Application No. PCT/US02/41182
filed December 20, 2002

PRELIMINARY AMENDMENT

The Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

Prior to examination on the merits, please amend the above-identified application as follows: